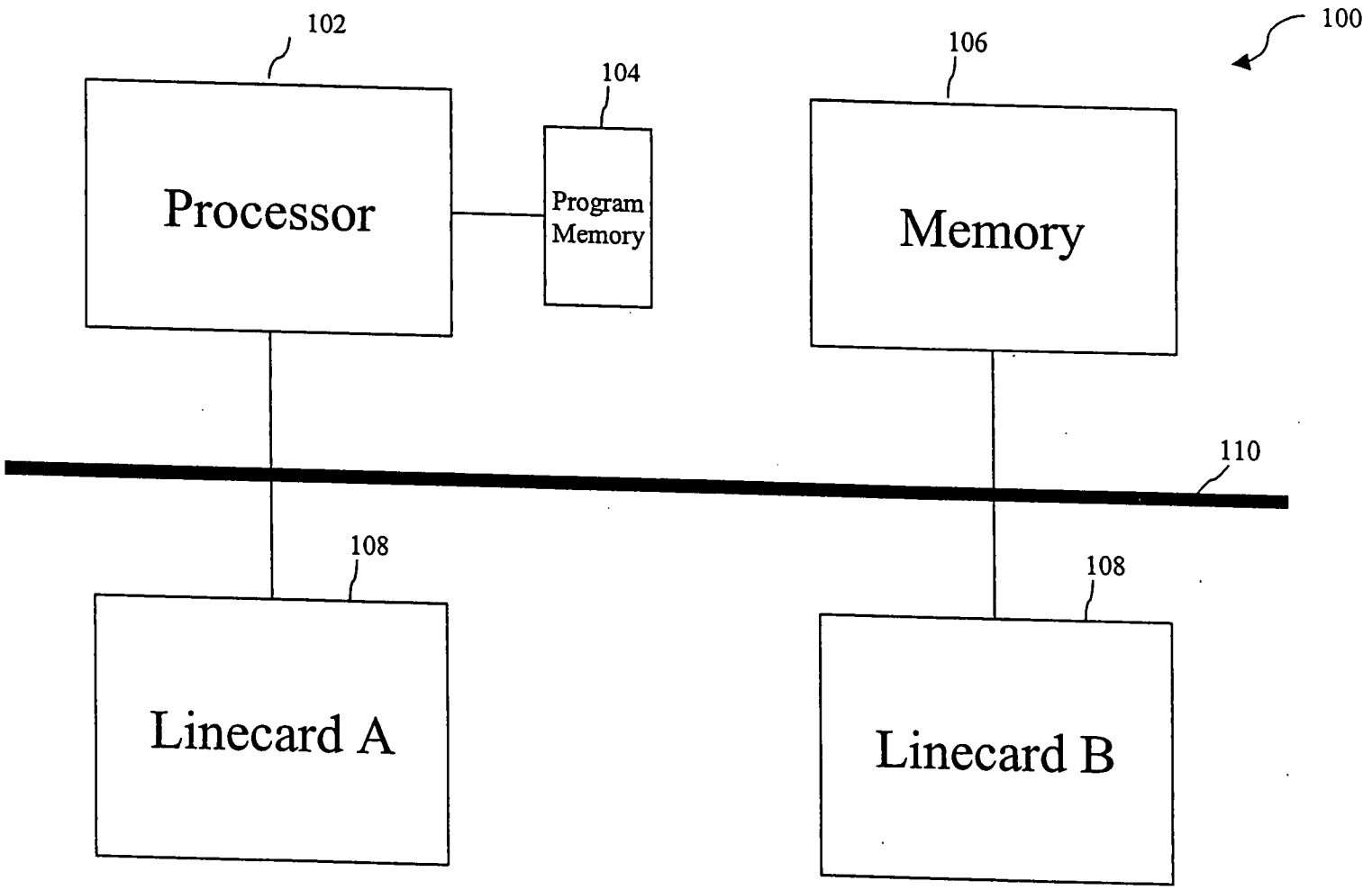
102
104
106
100
Processor
Program Memory
Memory
110
108
108
Linecard A
Linecard B

Fig. 1

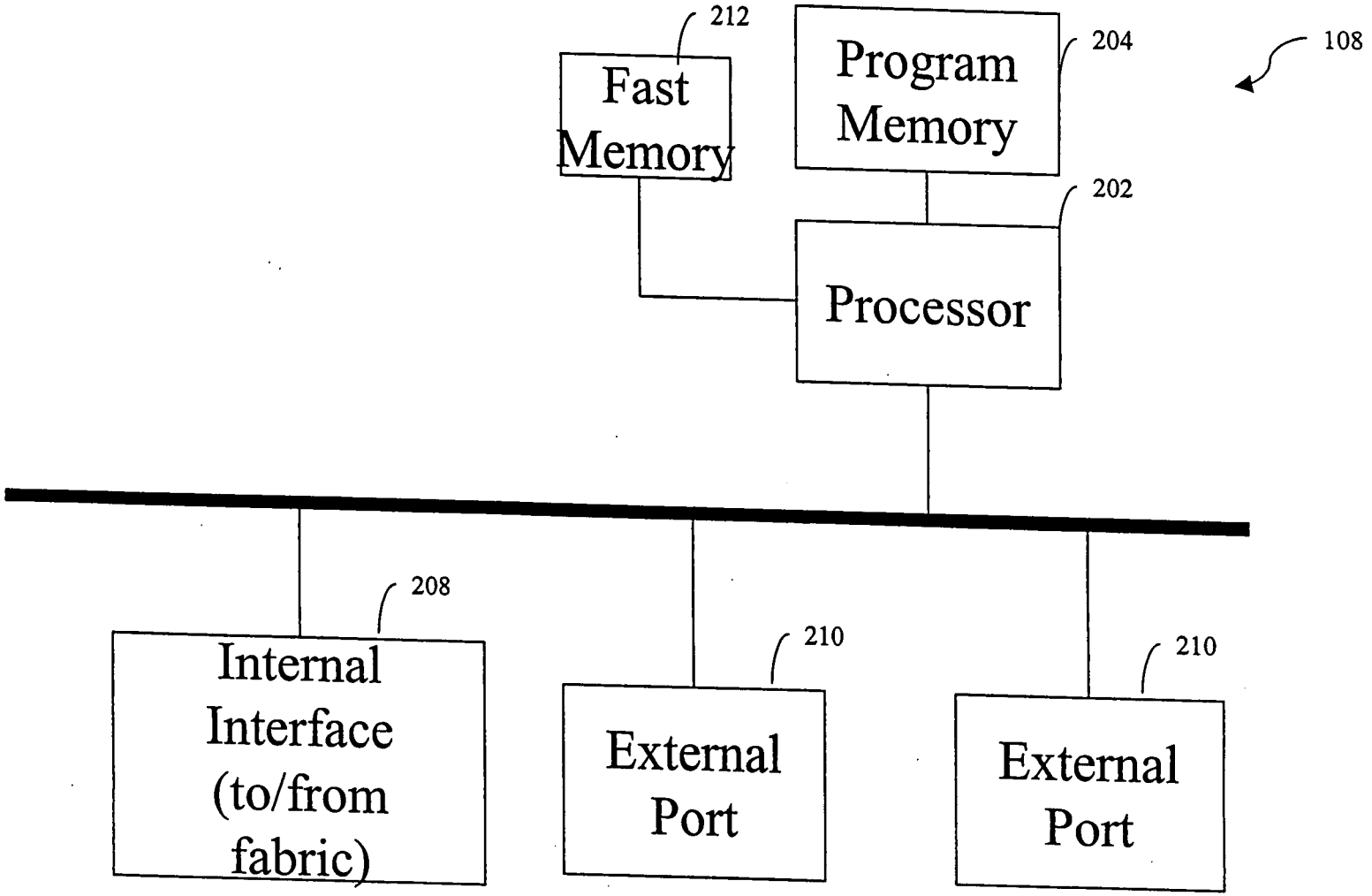
212
Fast Memory
Program Memory
204
108
202
Processor
208
Internal Interface (to/from fabric)
210
External Port
210
External Port

Fig. 2

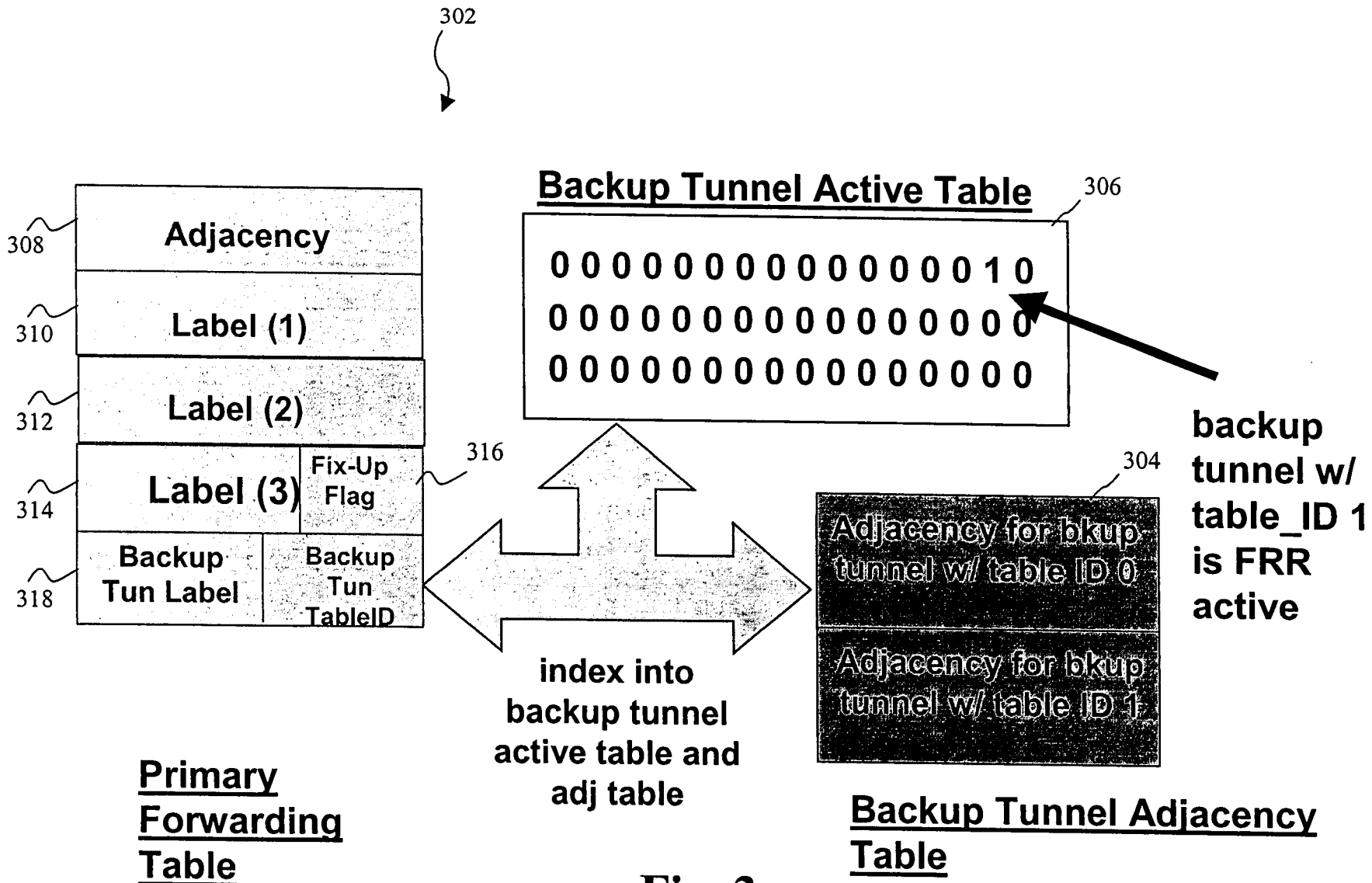
302
Backup Tunnel Active Table
306
0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
backup tunnel w/ table_ID 1 is FRR active
308
Adjacency
310
Label (1)
312
Label (2)
314
Label (3)
Fix-Up Flag
316
318
Backup Tun Label
Backup Tun TableID
304
Adjacency for bkup tunnel w/ table ID 0
Adjacency for bkup tunnel w/ table ID 1
index into backup tunnel active table and adj table
Primary Forwarding Table
Backup Tunnel Adjacency Table

Fig. 3

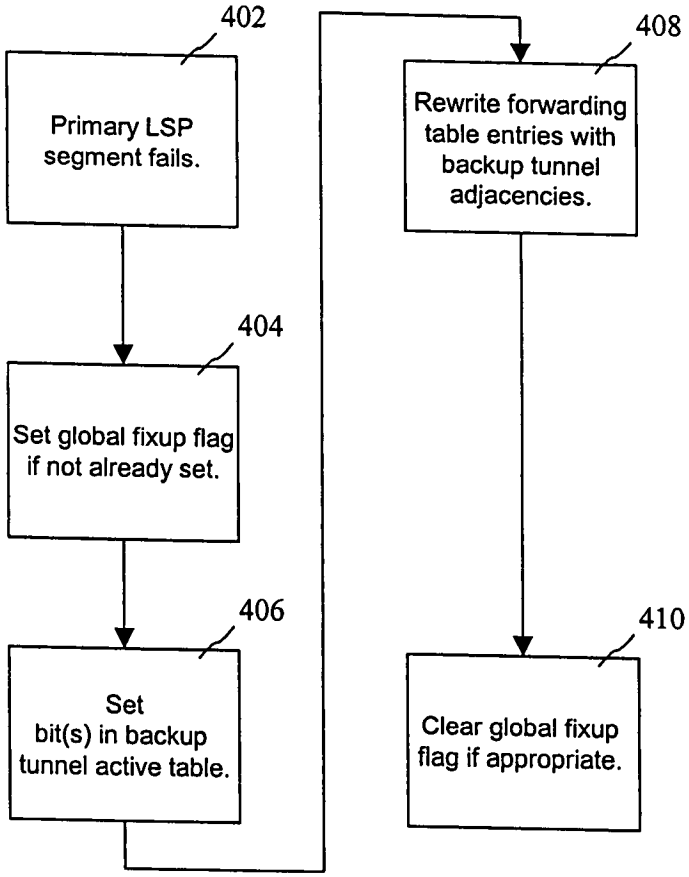
402
Primary LSP segment fails.
404
Set global fixup flag if not already set.
406
Set bit(s) in backup tunnel active table.
408
Rewrite forwarding table entries with backup tunnel adjacencies.
410
Clear global fixup flag if appropriate.

Fig. 4

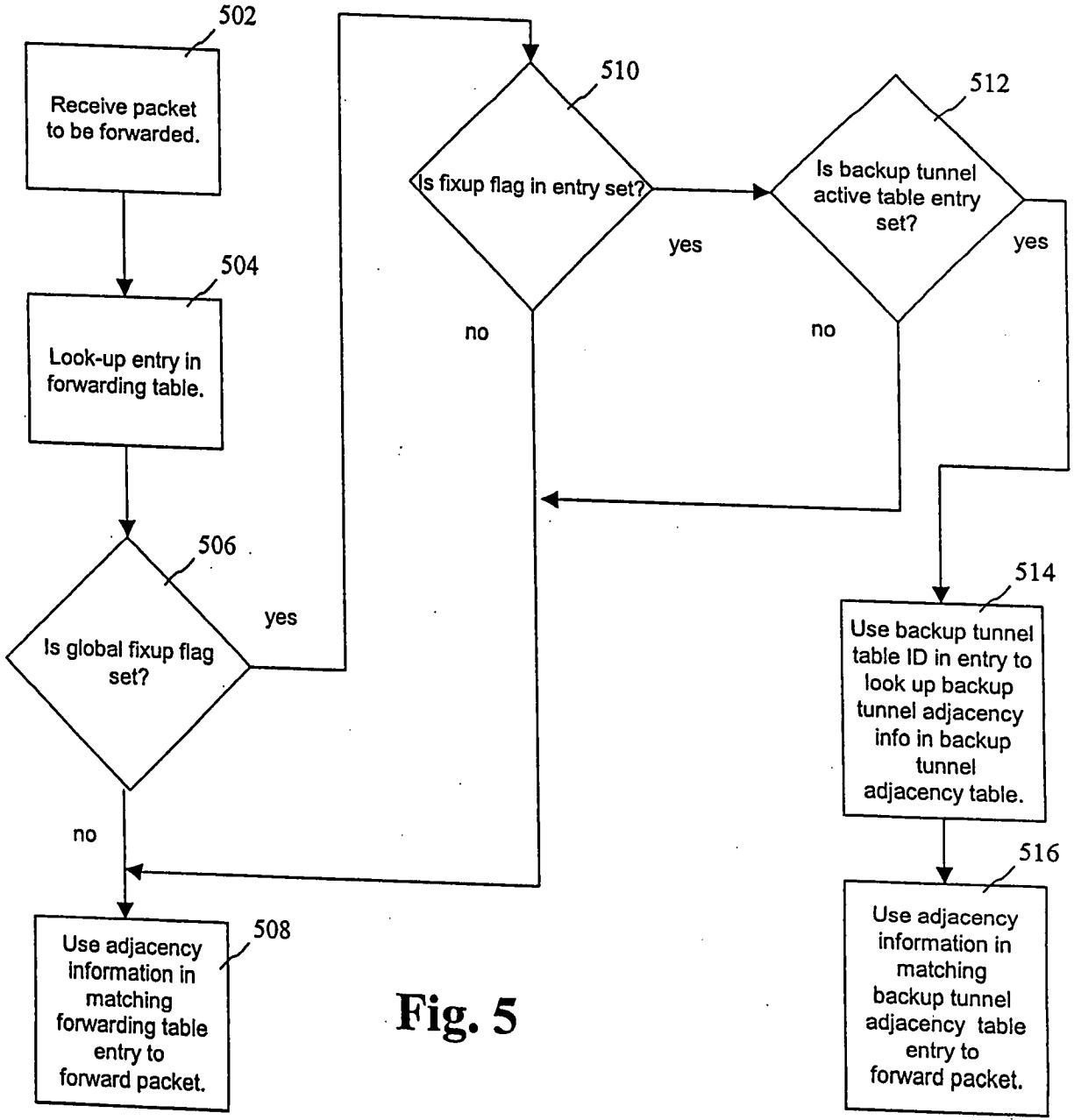
502
Receive packet to be forwarded.
504
Look-up entry in forwarding table.
506
Is global fixup flag set?
yes
no
510
Is fixup flag in entry set?
yes
no
512
Is backup tunnel active table entry set?
yes
no
514
Use backup tunnel table ID in entry to look up backup tunnel adjacency info in backup tunnel adjacency table.
516
Use adjacency information in matching backup tunnel adjacency table entry to forward packet.
508
Use adjacency information in matching forwarding table entry to forward packet.

Fig. 5